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DEPARTMENT OF ELECTRICAL - University of Central Florida LABORATORY MANUAL DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING UNIVERSITY OF CENTRAL FLORIDA EEE 3342 Digital Systems Revised August 2012 . CONTENTS Safety Rules and Operating Procedures Introduction Experiment #1 XILINX FPGA Tools Experiment #2 Simple Combinational Logic ... Open drinks and food are not allowed near the lab benches. 4. ASIC Design and FPGA-Verilog HDL-Lab Manual - Docsity This lab manual is for Verilog HDL course by Rehman Malik at Quaid-i-Azam University. It includes: ASIC, Design, FPGA, Verilog, HDL, Simulations, Experiments.

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